0/009750

531 Rec'd PCT/PTO 12 DEC 2001

Docket No.: 43890-555

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Toyonori KANETAKA, et al.

Serial No.:

Group Art Unit:

Filed: December 12, 2001

Examiner:

For:

METHOD OF MANUFACTURING CHIP INDUCTOR

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner for Patents and Trademarks Washington, D. C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed with the application and no certification or fee is required.

A copy of the foreign search report is attached for the Examiner's information. Please note this is a PCT application in the entry of the National Phase in the U.S. However, to ensure that these references are available to the Examiner, we are providing copies of these references herewith. Since the Search Report was from the U.S. JPO or EPO search authorities, copies of these references should have been supplied to the USPTO under Serial No.:

10/009750 531 Rec'd PCT/PTO 12 DEC 2001

the trilateral agreement and are believed to be in the file of the above identified application and readily available to the Examiner. Therefore it is believed that Applicants have met all requirements regarding duty of disclosure under 37 CFR 1.56. Acknowledgement and consideration of these documents are respectfully requested.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

Michael E. Fogarty

Registration No. 36,139

600 13th Street, N.W. Washington, DC 20005-3096 (202) 756-8000 MEF:prp

Date: December 12, 2001 Facsimile: (202) 756-8087

531 7.000	SHEET 1 OF 1
OCKET NO. • 555	serial no. 10/009750

INFORMATION DISCLOSURE ATTY. DOCK 43890-55 CITATION IN AN **APPLICATION** APPLICANT Toyonori KANETAKA, et al. FILING DATE GROUP (PTO-1449) December 12, 2001 U.S. PATENT DOCUMENTS EXAMINER'S FILING DATE INITIALS PATENT NO. DATE NAME CLASS **SUBCLASS** FOREIGN PATENT DOCUMENTS **EXAMINER'S** Translation INITIALS PATENT NO. DATE **COUNTRY** CLASS SUBCLASS 11-3820 1/6/1999 Japan (w/ English Abstract) 10-125533 5/15/1998 Japan (w/ English Abstract) 2000-64094 Japan (w/ English Abstract) 2/29/2000 2000-58336 2/25/2000 Japan (w/ English Abstract) 2000-30952 1/28/2000 Japan (w/ English Abstract) 62-286211 Japan (w/ English Abstract) 12/12/1987 11-260647 9/24/1999 Japan (w/ English Abstract) 8-78290 3/22/1996 Japan (w/ English Abstract) 6-96952 4/8/1994 Japan (w/ English Abstract) OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.) **EXAMINER** DATE CONSIDERED